

Case Study: Post Silicon Validation

OVERVIEW

HSIO PCIe Compliance, Characterization

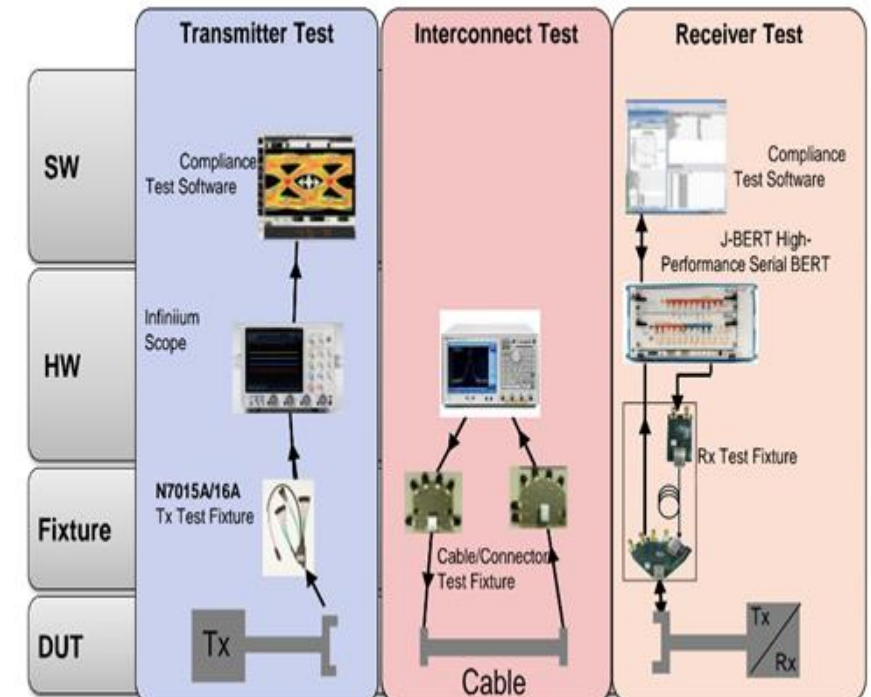
ENGAGEMENT DETAILS

SCOPE:

- Customer provided complete setup ,need to validate DUT and test all compliance testing for HSIO PCIe
- PCIe 4.0 Tx & Rx Compliance Testing
- PCIe 3.0 Tx & Rx Compliance Testing
- PCIe PLL Testing
- PCIe Tx and Rx Link
- Equalization with JBERT

VALUE DELIVERED

- Responsibility on release complete validation report and compliance test report
- Validation across different process corner, Voltage and Temperature (PVT Characterization)



Case Study: Post Silicon Electrical validation

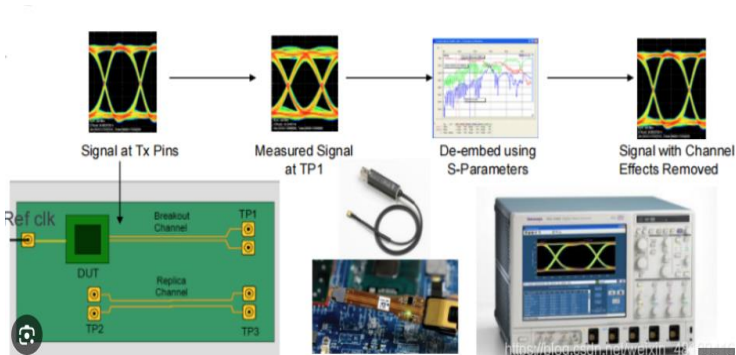
OVERVIEW

Electrical Testing



ENGAGEMENT DETAILS

- **SCOPE:**
 - Validating USB PHY
 - Generating Test Validation plan for USB Modules
 - Developing the test procedure and automating it for USB2.0, 3.0 and PCIe as per validation plan
 - Involved in initial Wakeup of module, functional testing after silicon arrival
 - Carried out Electrical measurements both Tx and Rx



VALUE DELIVERED

- Validation different process corner, Voltage and temperature
- Characterization test report generation

